

ABSTRACT OF THE DISCLOSURE

5 A microprocessor includes a program control unit controlling fetch of
an instruction code, an instruction decode unit decoding the fetched
instruction code, an address operation unit operating an address of a
memory on the basis of the result of decoding by the instruction decode unit
and a data operation unit executing data transfer between a control
register and a work register and data transfer between the work register
and an X memory in correspondence to a single push instruction.
10 Therefore, data stored in the control register incapable of directly pushing
data on the memory can be pushed with a single push instruction.